



Attorney Docket No. 1341.1075

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Takeshi SHIMOYAMA

Application No.: 09/739,219

ATTENTION

Confirmation No.: 8512

CERTIFICATE OF CORRECTION

Filed: December 19, 2000

BRANCH

U.S. Patent No.: 7,346,161

Issued: March 18, 2008

For: METHOD AND APPARATUS FOR DESIGNING CIPHER LOGIC, AND A COMPUTER
PRODUCT

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate

JUL 21 2008

of Correction

Sir:

Patentee respectfully requests that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistakes shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: July 17 2008

By: Mark J. Henry

Mark J. Henry
Registration No. 36,162

1201 New York Ave, N.W., 7th Floor
Washington, D.C. 20005
Telephone: (202) 434-1500
Facsimile: (202) 434-1501

RECEIVED-USPTO
Patent Publication

JUL 21 2008

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.
(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 7,346,161

DATED : March 18, 2008

INVENTOR(S) : Takeshi SHIMOYAMA

Column 10, Line 38, after "claim 1" insert --,--.

Column 12, Line 16, change "in putting" to --inputting--.

MAILING ADDRESS OF SENDER:

STAAS & HALSEY LLP
1201 New York Ave., N.W.
7th Floor
Washington, DC 20005

PATENT NO. 7,346,161

No. of add'l copies
@ 50¢ per page

⇒

RECEIVED-USPTO
Patent Publication

JUL 21 2008